

WHAT IS CLAIMED IS:

1. A liquid crystal display comprising:

a gate electrode line including a gate electrode formed on an
5 insulating substrate;

a source electrode line including a source electrode
intersected with said gate electrode line via an insulating film;

a thin film transistor located in a vicinity of a portion in
which said gate electrode line is intersected with said source electrode
10 line;

two drain electrode lines, each including two drain electrodes
in said thin film transistor, said drain electrode line being connected
with a pixel electrode;

wherein said thin film transistor includes said two drain
15 electrode lines located on both sides of said source electrode; said two
drain electrodes are formed at a place where each end portion of said two
drain electrode lines opposed to said source electrode is superposed with
said gate electrode line.

2. The liquid crystal display of Claim 1, wherein an area of
20 a region where said gate electrode line is superposed with one of said
two drain electrode lines is substantially identical to an area of a
region where said gate line is superposed with the other one of said
two drain electrode lines.

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3. The liquid crystal display of Claim 2, wherein a length
of a region in a channel lengthwise direction of said thin film

transistor where said gate electrode line is superposed with one of said two drain electrodes is substantially identical to a length of a region in a channel lengthwise direction of said thin film transistor where said gate electrode line is superposed with the other one of said two drain
5 electrode lines.

4. The liquid crystal display of claim 3, wherein said length of said area in the channel lengthwise direction is such a length as to prevent a current characteristics from degradation in said thin
10 film transistor.

5. The liquid crystal display of any one of claims 1, 2, 3 and 4, wherein said drain electrode is formed in whole part of one end of said drain electrode line in a channel widthwise direction where
15 said drain electrode line is superposed with said gate electrode line.

6. The liquid crystal display of any one of claims 1, 2, 3 and 4, wherein said drain electrode is formed at a portion where a part of one end of said drain electrode line in the channel widthwise
20 direction opposed to said source electrode is superposed with said gate electrode line on both sides of said source electrode.

7. The liquid crystal display of any one of Claims 1, 2, 3, 4, 5 and 6, wherein a lead portion of said source electrode line extended
25 to said source electrode from said source electrode line is provided with a semiconductor film situated above or below said gate electrode line via an insulating film in reference to the insulating substrate.

8. The liquid crystal display of any one of Claims 1, 2, 3, 4, 5, and 7, wherein a lead portion of said source electrode line extended to said source electrode from said source electrode line is provided with a semiconductor film, said semiconductor film being situated
5 above said lead portion of said source electrode line or below the same in reference to the insulating substrate.

9. The liquid crystal display of Claims 1, 2, 3, 4, 5, 6, 7 or 8, wherein said two drain electrodes opposed to said source electrode
10 on both sides of said source electrode are connected with each other in the region between said two drain electrode lines and said pixel electrode, said drain electrodes being connected with said pixel electrode by a single part of said drain electrodes.

10. The liquid crystal display of Claims 1, 2, 3, 4, 5, 6, 7, 8 or 9, wherein said drain electrode line is formed of a same film as that of said pixel electrode.

11. A method for manufacturing a liquid crystal display
20 comprising steps of:

forming a gate electrode line pattern on an insulating substrate;

forming a semiconductor film covering said gate electrode line;

25 depositing a conductive film serving as source/drain electrodes on said insulating film; and

subjecting said deposited conductive film to patterning in

such a manner that two drain electrodes are formed in a portion where each end of two drain electrodes opposed to said source electrode is superposed in a channel lengthwise direction with said gate electrode line on both sides of said source electrode.

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12. A method for manufacturing a liquid crystal display comprising steps of:

depositing a conductive film on an insulating substrate serving as source/drain electrodes;

10 subjecting said deposited conductive film to patterning in such a manner that two drain electrodes are formed in a portion where each part of the two drain electrode lines extending in a channel lengthwise direction is superposed with a gate electrode line, said drain electrode lines being opposed to said source electrode on both
15 side surfaces;

forming a semiconductor film on said source/drain electrodes;

forming an insulating film in such a manner as to cover said semiconductor film; and

20 forming a gate electrode pattern on said insulating film.

13. The method of Claim 11 or 12, further comprising a step of forming a pixel electrode pattern connected with said drain electrode, wherein said drain electrode is formed in said step of
25 forming said pixel electrode pattern.